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| **APPLICANTS: Saikawa Takeshi; Maeno Ryohei; Okudaira Sadayuki; Saito Tetsuo; Tamaru Tsuyoshi; Ohmori Kazutoshi; | | | | | |
| **CONTINUING DATA VERIFIED: NONE | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-140622 05/11/2001 | | | | | |
| PG-PUB <input type="checkbox"/> | | DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials <i>[Signature]</i> | | | | ATTORNEY DOCKET NO XA-9635 | |
| TITLE : Manufacturing method of semiconductor device | | | | U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94) | |

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|---|-----------|---------------------------|-------------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drawn | Figs. Drawn |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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